

## WEST Search History





DATE: Saturday, December 22, 2007

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		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L12	(visual\$7 or gui or graphic\$4 or display\$4) same (dsp or processor or super\$1scalar or multi\$1processor) same (pipeline) same (debug\$4 or break\$1point or break)	73
<input type="checkbox"/>	L11	(visual\$7 or gui or graphic\$4 or display\$4) same (dsp or processor or super\$1scalar or multi\$1processor) same (pipeline) and (debug\$4 or break\$1point or break)	680
<input type="checkbox"/>	L9	lsi.asn. and (visual\$7 or gui or graphic\$4 or display\$4) same (dsp or processor or super\$1scalar or multi\$1processor) same (schedul\$4 or pipeline or stag\$3) and (debug\$4 or break\$1point or break)	1
<input type="checkbox"/>	L6	703/3,13-28.ccls. and (visual\$7 or gui or graphic\$4 or display\$4) same (dsp or processor or super\$1scalar or multi\$1processor) same (schedul\$4 or pipeline or stag\$3) and (debug\$4 or break\$1point or break)	38
<input type="checkbox"/>	L5	717/\$.ccls. and (visual\$7 or gui or graphic\$4 or display\$4) same (dsp or processor or super\$1scalar or multi\$1processor) same (schedul\$4 or pipeline or stag\$3) and (debug\$4 or break\$1point or break)	79
<input type="checkbox"/>	L4	(visual\$7 or gui or graphic\$4 or display\$4) same (dsp or processor or super\$1scalar or multi\$1processor) same (throughput) same (schedul\$4 or pipeline or stag\$3) and (debug\$4 or break\$1point or break)	19
<input type="checkbox"/>	L3	717/100-135.ccls. and (visual\$7 or gui or graphic\$4 or display\$4) same (disassembly or assembly) same (dsp or processor or super\$1scalar or microprocessor) same (highlight\$3 or succeed\$4 or clock or cycle or designat\$4 or non\$1designat\$3 or schedul\$4 or pipeline or stag\$3)	7
<input type="checkbox"/>	L1	717/100-135.ccls. and (visual\$7 or gui or graphic\$4 or display\$4) same ( pipelin\$4) same (dsp or processor or super\$1scalar) same (debug\$5 or break\$1point)	5

END OF SEARCH HISTORY